

FIG. 1

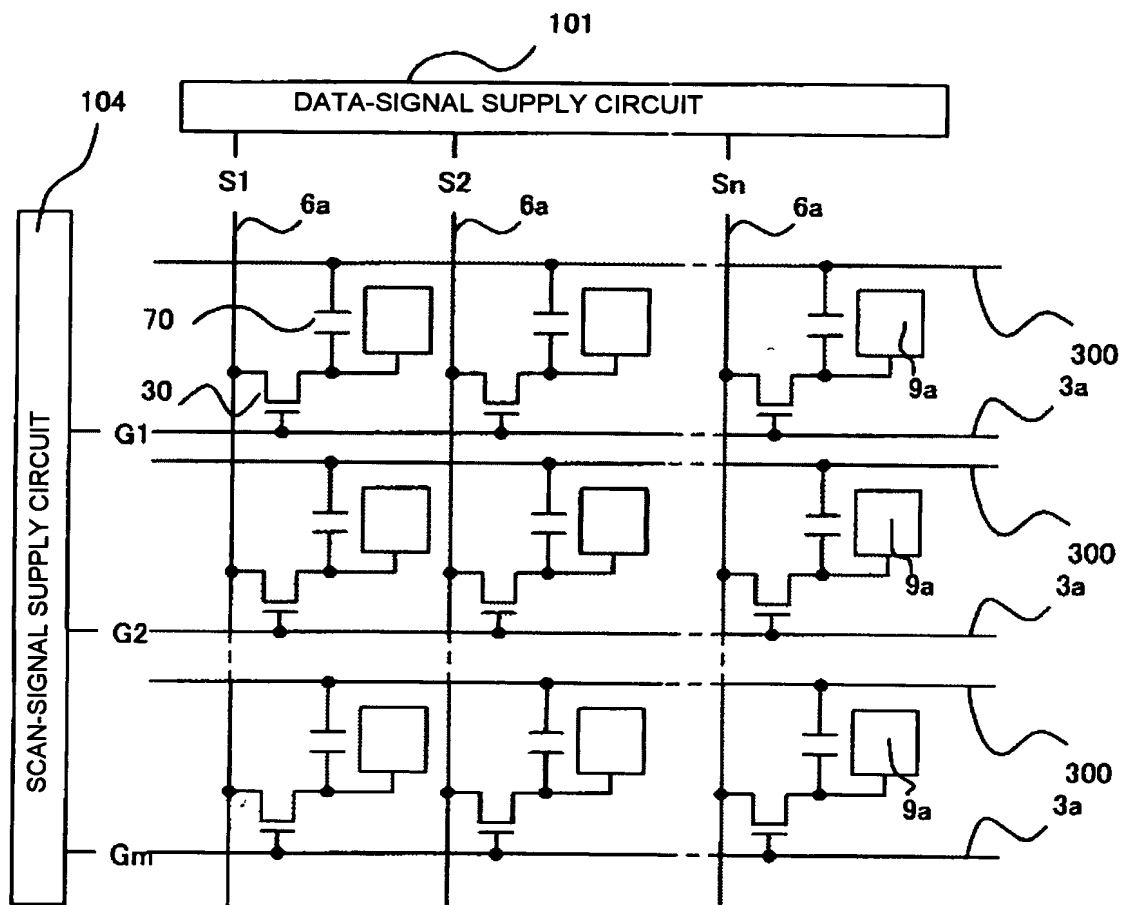


FIG. 2

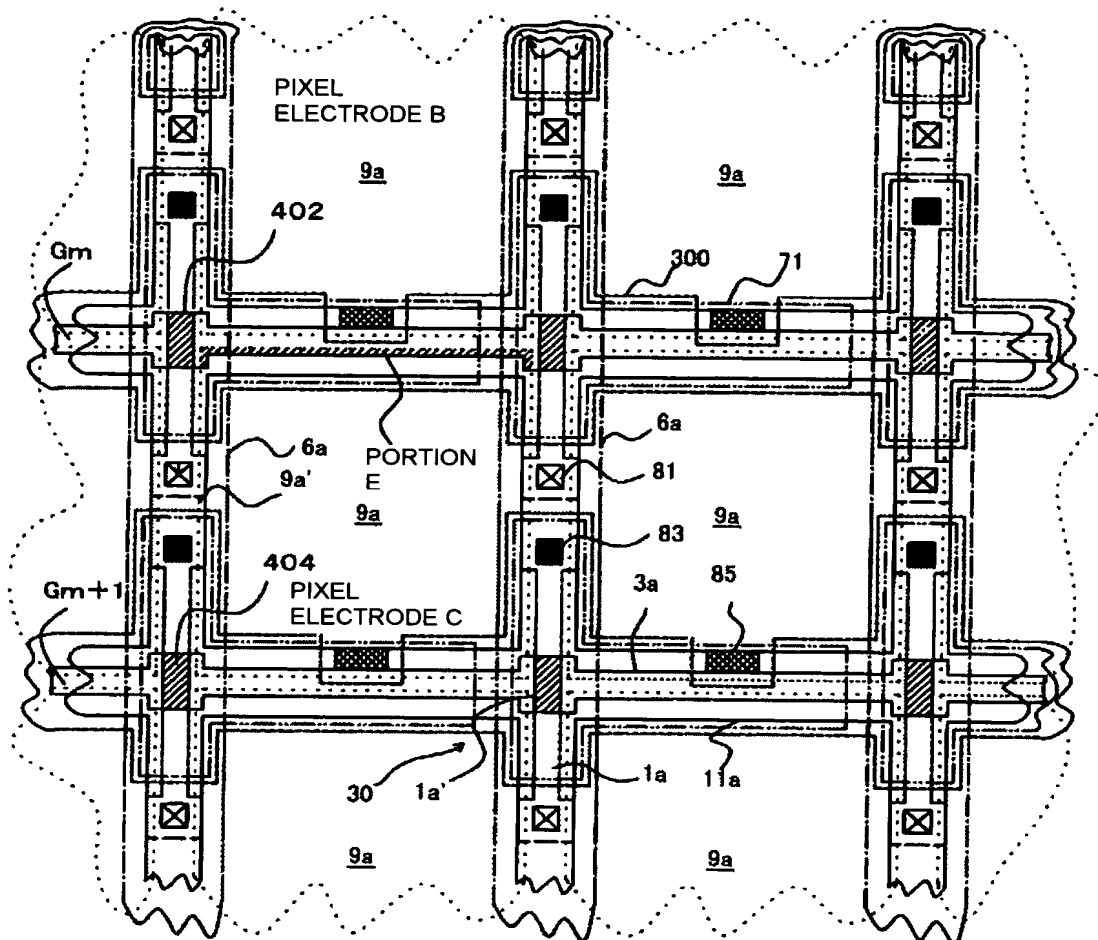
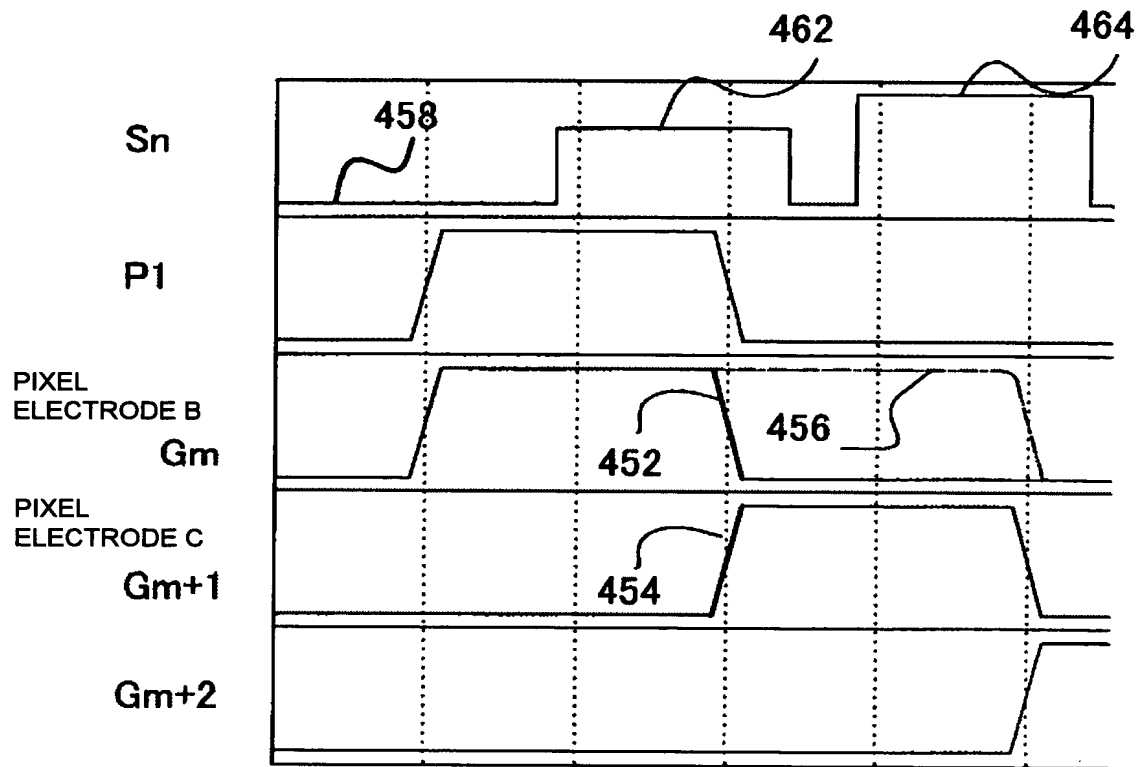


FIG. 3



The figure shows a multi-bit DAC circuit. At the top, a reference voltage \$V\_{ref}\$ is connected to a resistor network (534) and a current source (536). A feedback loop includes a resistor (532) and a transconductance stage (524) with input \$V\_n\$. The output of the DAC is connected to a summing junction (520) which also receives inputs from a DAC (522) and a pulse generator circuit (526). The pulse generator circuit is driven by a clock signal (528) and produces a series of pulses (530) that are summed at the junction (520). The output of the summing junction is connected to a multi-bit digital-to-analog converter (504), which consists of a series of comparators (506) and transistors (508) connected to ground.

FIG. 5

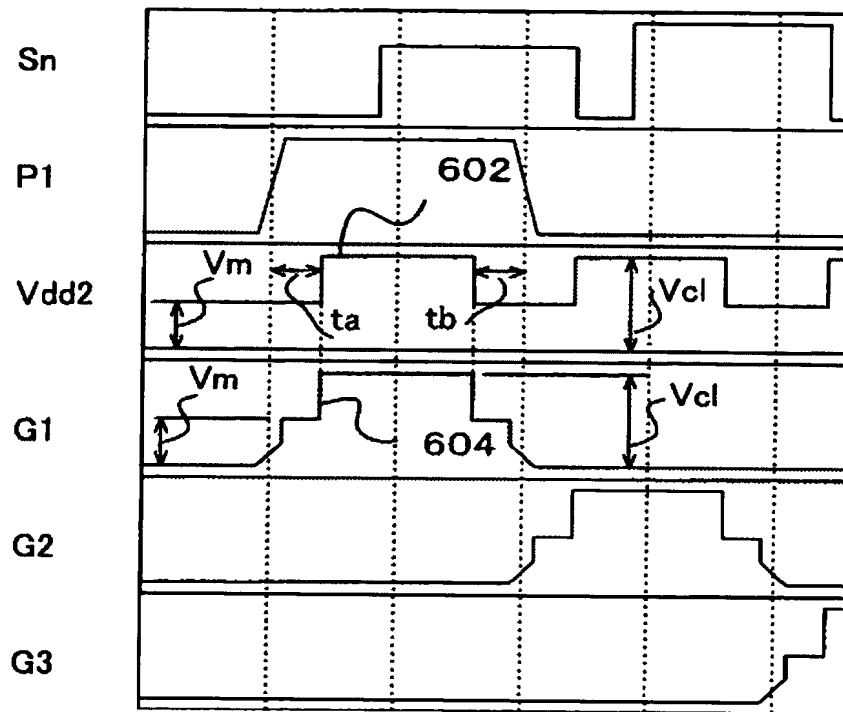


FIG. 6

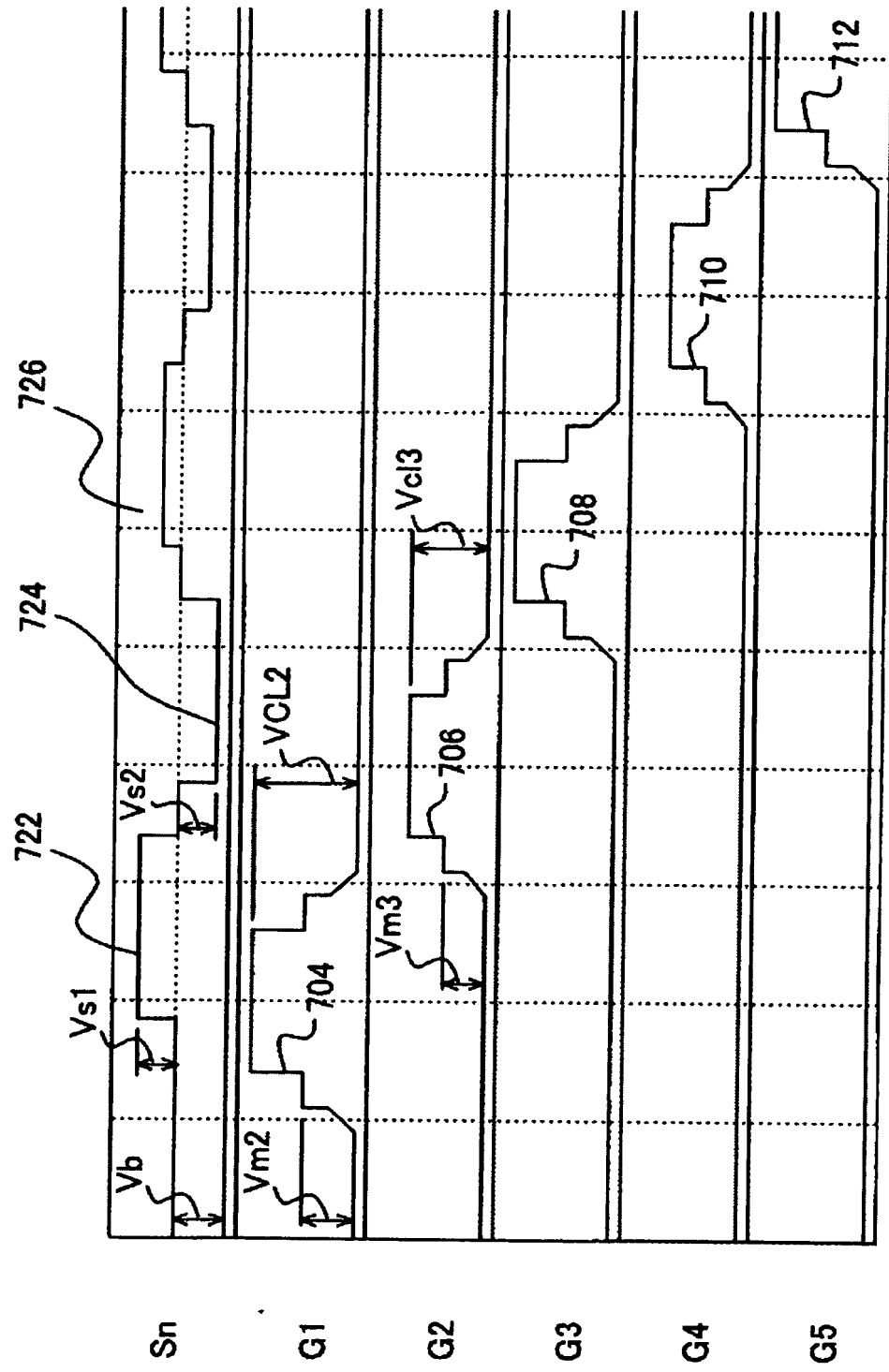


FIG. 7

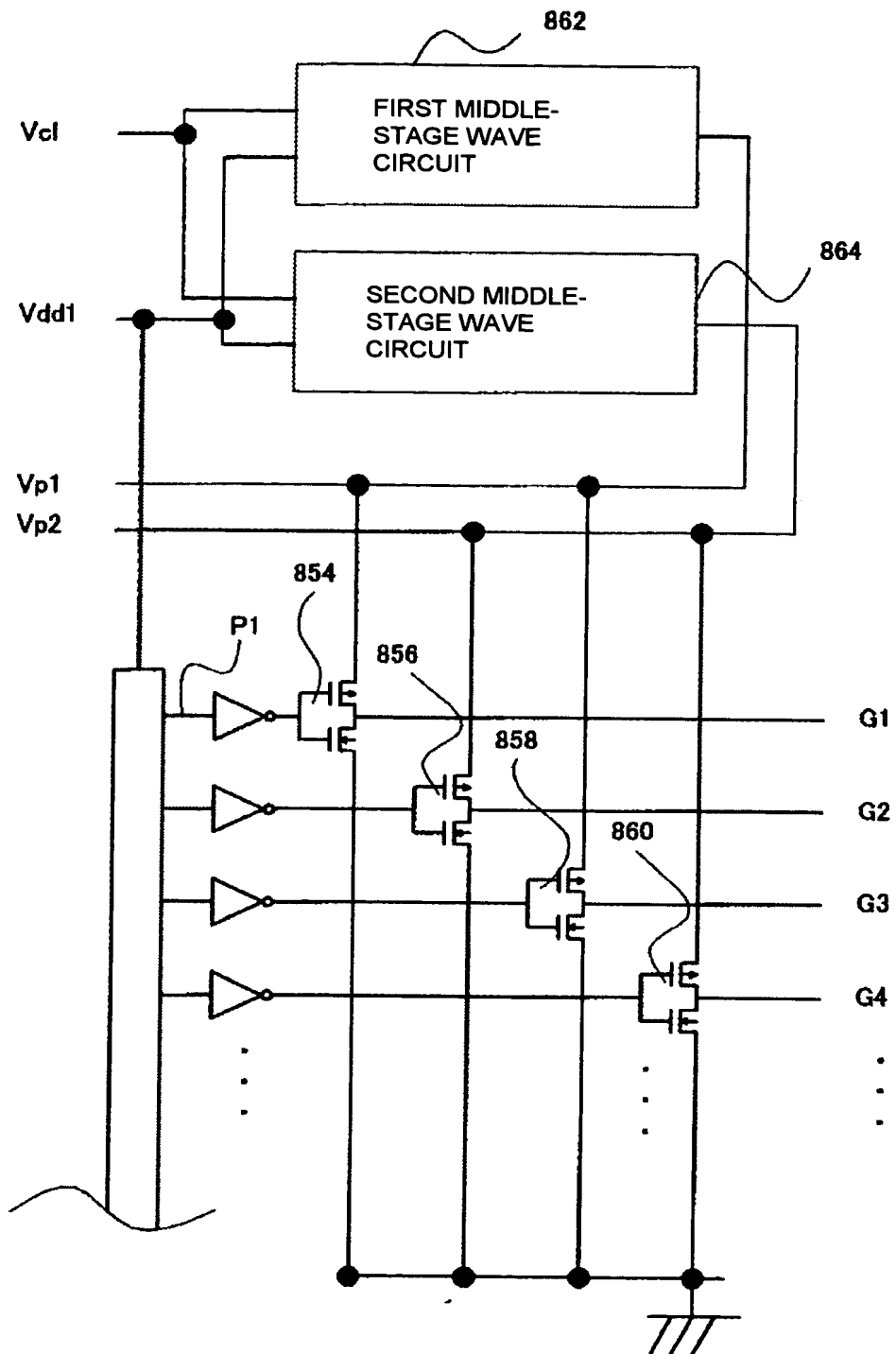


FIG. 8

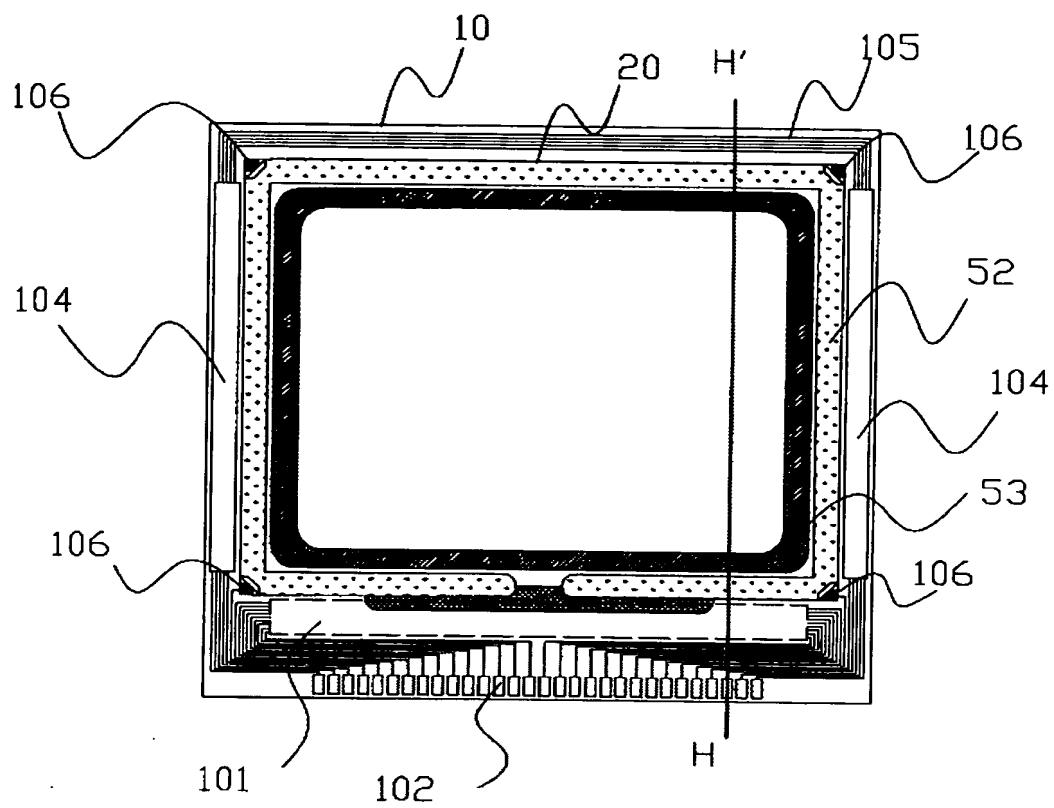




FIG. 9

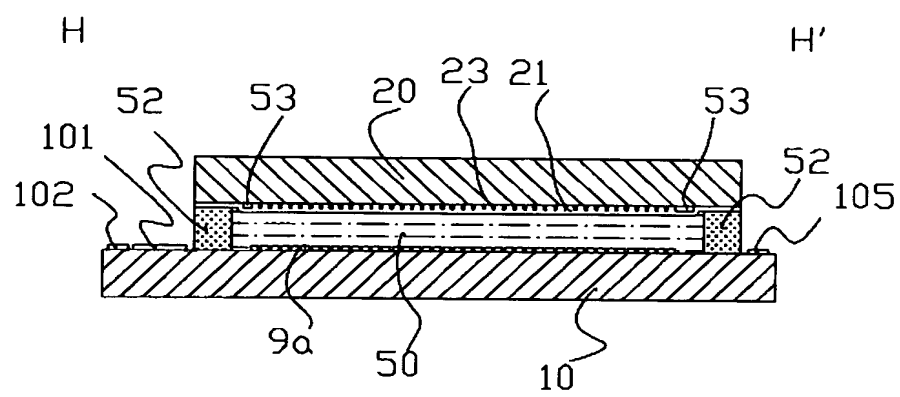


FIG. 10

